



## PATENT ABSTRACTS OF JAPAN

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**(54) PACKET PROCESSING UNIT****(57) Abstract:**

**PROBLEM TO BE SOLVED:** To attain packet processing at high speed without giving the effect of packet processing of each layer on processing by other layers with respect to a packet processing unit having hierarchical communication protocols.

**SOLUTION:** An idle capacity report section 102 informs a storage condition discrimination section 303 about an idle capacity 93 of a reception buffer 101 of a host layer 100, the storage condition discrimination section 303 decides the size of a big packet 92 on the basis of the idle capacity 93, a reconfiguration buffer processing section 301 reconfigures a plurality of received packets 91 into one big packet and transmits it to the reception buffer 101 in this configuration of the packet processing unit. An in-reverse direction packet information read circuit that is placed in a subordinate

layer 300 and detects the idle capacity 93 on the basis of the information in the reverse direction packet from the host layer 100 is adopted for the idle capacity notice section 102. Or, an application layer is used for the host layer 100 and the big packet 92 may directly be sent to the reception buffer 101 not through a buffer of a transport layer.

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